

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Group Art Unit: 2823

Examiner: George R. Fourson III

In Re PATENT APPLICATION Of:

Applicant(s) : Yasuo TANAKA

Serial No. : 09/660,484

Filed: September 12, 2000

METHOD OF MANUFACTURING

SEMICONDUCTOR DEVICE

Attorney Ref. : OKI 262

April 20, 2005

AMENDMENT AFTER

**ALLOWANCE** 

Mail Stop Issue Fee Commissioner for Patents

P.O. Box 1450

Alexandria, VA 22313-1450

Sir:

This Amendment is submitted in response to the Notice of Allowance dated March 10, 2005. It is respectfully requested that the allowed claims be amended as follows pursuant to 37 CFR §1.312.

FEE ENCLOSED:s Please charge any further lee to our Deposit Account Vo. 18-0002